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1 Exploring the switch design space in a CC-NUMA multiprocessor environment

Pirvu, M.; Ni, N.; Bhuyan, L.;

Parallel and Distributed Processing Symposium, 2000. IPDPS 2000. Proceedings.

14th International , 1-5 May 2000

Pages:703 - 710

[\[Abstract\]](#) [\[PDF Full-Text \(244 KB\)\]](#) IEEE CNF

2 A 9.6 GigaByte/s throughput plesiochronous routing chip

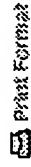
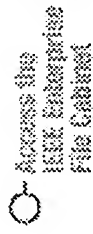
Mu, A.; Larson, J.; Sastry, R.; Wicki, T.; Wilcke, W.W.;

Compcon '96. 'Technologies for the Information Superhighway' Digest of

Papers , 25-28 Feb. 1996

Pages:261 - 266

[\[Abstract\]](#) [\[PDF Full-Text \(492 KB\)\]](#) IEEE CNF



3 Analysis of system performance by changing the ring architecture on the dual ring CC-NUMA system

Joo Beom Yun; Seong Tae Jhang; Chu Shik Jhon; Cheol Won Lee; Parallel and Distributed Systems, 2002. Proceedings. Ninth International Conference on , 17-20 Dec. 2002
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[Abstract] [PDF Full-Text (318 KB)] IEEE CNF

4 Sensitivity of parallel applications to large differences in bandwidth and latency in two-layer interconnects

Plaatz, A.; Bal, H.E.; Hofman, R.F.H.; High-Performance Computer Architecture, 1999. Proceedings. Fifth International Symposium On , 9-13 Jan. 1999
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Xiaodong Zhang; Yong Yan; Parallel and Distributed Systems, IEEE Transactions on , Volume: 6 , Issue: 12 , Dec. 1995
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6 Hot spot analysis in large scale shared memory multiprocessors

Harzallah, K.; Sevcik, K.C.; Supercomputing '93. Proceedings , 15-19 Nov. 1993
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[Abstract] [PDF Full-Text (596 KB)] IEEE CNF

7 Modeling data migration on CC-NUMA and CC-COMA hierarchical ring architectures

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1994., MASCOTS '94., Proceedings of the Second International Workshop on , 31
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[\[Abstract\]](#) [\[PDF Full-Text \(296 KB\)\]](#) **IEEE CNF**

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